

One Week FDP on “Mentor Graphics Tools”

Department of Electronics & Communication Engineering, Narayana Engineering College, Nellore, organized a One Week FDP on “Mentor Graphics Tools” from 1st to 7th March 2018 in association with CoreEl Technologies, Bangalore. In this FDP, 41 faculty members from various engineering colleges have participated. Resource persons were Mrs.M Suhasini , Application Engineer, Mr. B.Nagendra, Application Engineer from CoreEl Technologies and Mr.Phani Bhusan professor from PBR VITS, Kavali.

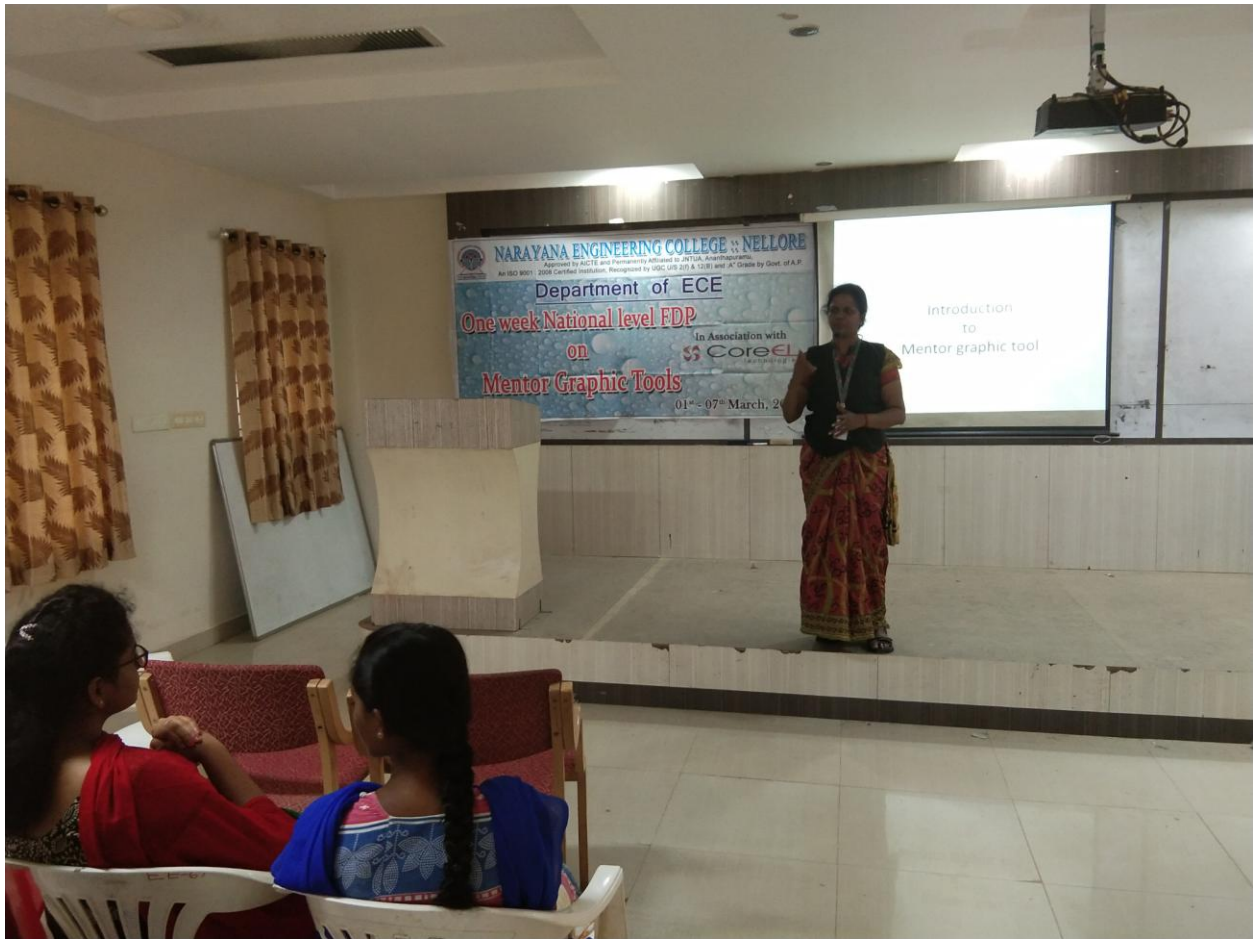


Inaugural function of the workshop started with a welcome address by Dr. K Murali, HOD-ECE followed by lighting of lamp by the Principal Dr. G Srinivasulu Reddy. HOD of ECE presented the theme of the workshop and discussed on the importance of VLSI Backend Tools like Mentor graphics, cadence, Tanner tools etc. Later, the Principal of NECN addressed the staff members and stated that the staff members should develop awareness on latest technologies in electronics, which will be helpful for the growth of personnel and students.

On the second day of FDP, Mr.B Nagendra has given training on analog signal design using mentor graphics tools. And on the third day of FDP, Mr.B.Nagendra has presented the mixed signal design using mentor graphic tools



During the 4th day of the FDP, Mr.V.Phani Bhusan made presentation on current trends in VLSI design. He discussed the role of mentor, graphics tools, and also demonstrated the design flow using mentor graphic tools and exercises including design and implementation.



During the 5th day of the FDP, Mrs.M Suhasini discussed VLSI IC design flow and given hands on training to the participants.

In Last Day, guests were felicitated and participation certificates, appreciation certificates were distributed to the participants and the organizers respectively..